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Technical Committee/ Plenary Speakers

- Mr. Prasant Kumar Panda, Senior Data Scientist, TCS
 - Prof. Pradipta Kumar Nanda, SOA University
 - Dr. Hemanta Kumar Rath, Senior Data Scientist, TCS
 - Mr. Sudhansu Sekhar Naik, Intel Bangalore
- And other Professors from IIT/ NIT will review the papers.

Conference Co-ordinator (s)

- Dr. Aruna Tripathy, HOD IEE
- Dr. Kanhu Charan Bhuyan, Asst Prof, IEE, CET, BBSR
- Ms. Ananya Dastidar, Asst Prof, IEE, CET, BBSR
- Ms. Abhyarthana Bisoyi, Asst Prof, IEE, CET, BBSR
- Dr. K. Rama Devi, JNTUK
- Mrs. A. Rajani, JNTUK

Organizing Committee

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Contact Details

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Registration Details

Category	Fee (Rs.)
UG Students	1,000/-
PG Students	1,500/-
Research Scholars/ Academicians	2,000/-
Industry Personnel	4,000/-

Authors of selected papers should send registration form along with DD on or before 10th February 2020. The registration fees include local hospitality during the conference sessions and registration kit with conference proceedings. All other expenses (including accommodation) are to be borne by the participants. For presenting the paper in the conference one of the authors (in case of multiple authors) has to register by paying the requisite registration fee by the dates given below.

Submission of Paper

Prospective authors are invited to prepare full text paper (not exceeding 5 pages) in IEEE format available on http://www.ieee.org/conferences_events/conferences/publishing/templates.html

Authors are requested to send the soft copy of the paper to the organizers by email to vspice2020@gmail.com. The accepted and presented full papers only will be published in conference proceedings.

Important Dates

Last Date for Submission (full text paper)	30-12-2019 10.01.2020
Acceptance Notification	10-01-2020 20.01.2020
Online Registration	20-01-2020 30.01.2020
Camera Ready Copy: submission	20-01-2020 30.01.2020

*All DD's should reach by 30th January 2020 with the registration form.

Two Day National Conference
on
**VLSI Design, Signal Processing, Image Processing,
Communications & Embedded Systems
(VSPICE 2020)**
(26th -27th February 2020)

Sponsored by
TEQIP-III

(Technical Education Quality Improvement Program-III)



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IN ASSOCIATION WITH JNTU, Kakinada

ABOUT CET

The College of Engineering & Technology, Bhubaneswar was established by the Government of Odisha in 1981 to meet the growing technical man power need in the State. The college is located in the Techno-campus (an area of more than 100 Acres) at Kalinga Nagar, Bhubaneswar about 2.0 Kms. away from Khandagiri-Udayagiri caves. The college was shifted to the new Techno Campus in March, 2001. Equipped with high end research facilities with the presence of a High Performance Computing Lab for Artificial Intelligence, Deep Learning and Virtual Reality, Scanning Electron Microscope, 3D Printers to name a few. The college is NAAC 'A' accredited and got an academic autonomy status in September 2017. The college has received funds under three TEQIP programs.

ABOUT THE DEPARTMENT

The Department of Instrumentation and Electronics Engineering was established in 1992. It is equipped with sophisticated laboratories and well qualified faculty. The department is NBA accredited which caters to the different fields of electronics through its various labs in the field of Instrumentation, Communication, Process Control, VLSI, Embedded Systems and Signal Processing.

ABOUT JNTUK

Jawaharlal Nehru Technological University Kakinada is formed after quadrifacating JNTUH in the year 2008. University College of Engineering, Kakinada, is constituent college of this JNTU Kakinada, playing its role of imparting technological education and actively involving in research in the state of Andhra Pradesh. The Department of Electronics and Communication Engineering in this Institute is the first department established in the state of Andhra Pradesh. The department has expertise in Communication Engineering, Signal and Image processing, Antennas and VLSI Design.

OBJECTIVES OF THE CONFERENCE

The conference aims at providing an opportunity for knowledge sharing, exchange of research experience and ideas among Academia, Industry, Research Scholars, Scientists and Entrepreneurs for sustainable growth of the society. Original and high quality contributions are invited from prospective authors from related areas. The contributions must not be published elsewhere or submitted for publication. During the review period, Papers will be reviewed by eminent Researchers in the respective areas.

The Organizers cordially invite Abstracts and Full Research Papers for presentation from all over the academic and industry community to participate/present and publish their research papers in the National Conference on 26th & 27th of Feb, 2020, in association with JNTU Kakinada in the following scientific areas for presentations and discussions thereafter.

Call for Papers

VSPICE 2020 invites original submissions in the following broad areas:

- **VLSI / Micro and nanoelectronics/ /Instrumentation**
- **Signal and Speech Processing/ Machine Learning**
- **Image and Video Processing**
- **Communications (Wireless, Mobile Radar & Microwave Applications/ Antenna/ 4G-5G Mobile Networks/ Software Defined Radio/ Optical Communication)**
- **Embedded Systems Applications/ IoT**

Salient features of this conference:

- The conference will be organized in association JNTUK
- The Plenary Speakers and Technical Committee of the Conference will comprise of experts from the Industry and academia.
- The Conference will include a UG Student forum.
- The conference will also present an award for Best Paper in the UG and PG category respectively.

REGISTRATION FORM TEQIP - III SPONSORED

**Two Day National Conference
on
VLSI Design, Signal Processing, Image Processing,
Communications & Embedded Systems
(VSPICE 2020)
26th & 27th February 2020**

Name :

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Signature of the Applicant

This is to certify thatis a regular employee of our institution and is hereby sponsored for attending the Two Day National Conference on VLSI Design, Signal Processing, Image Processing, Communications & Embedded Systems (VSPICE 2020) at College of Engineering and Technology, Bhubaneswar in association with JNTUK from 26th to 27th February 2020.

**Signature with seal
(Director/ Principal)**