**Department of Computer Science & Application**

**College of Engineering and Technology, Techno Campus,**

**Kalinga Nagar, Ghatikia, Bhubaneswar**

**No.** \_\_\_\_169\_\_\_\_ /**CSA., Date** 04.09.18

**CALL FOR QUOTATION**

Sealed quotation are invited from the intending registered firms /Authorized license supplier for supply of 10 nos. of **VLSI Trainer Kit** for CSA department CET as specified below along with other terms and condition.Your quotation should reach the **to the Principal, College of Engineering & Technology, Kalinga Nagar, Bhubaneswar. by 24.09.18 on or before 4.00 p.m. positively** in a sealedcovered envelope **Super Scribing on the top: Quotation of 10 nos. of VLSI Trainer Kit for Department of CSA, CET, Bhubaneswar.** The quotation received after due date will not be entrained. The Principal, CET has the right to accept or to reject any or all of the quotation without assigning any reason thereof.

***Item Details***

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| **Sl. No** | **Item** | **Specification** |
| **1.** | **VLSI Trainer Kit** | 1. SPARTAN 3 (XC3550 FPGA) Module
2. 10 MHz, 5MHz, 1MHz and 100 Hz Clock generator.

 Power Selection.1. I /O Expansions through 26 pin FRC Connector to interface IC- XX series interface module.
2. 16 Toggle Input switch with LED indication.
3. 16 LED for output indication.

**Onboard Interfaces**1. 4 digit 7 segment displays.
2. ISP Programmer for Piggy Bug Module.
3. All IC’s are mounted on IC Sockets.
4. Bare board Tested Glass Epoxy SMOBC PCB is used.
5. User’s Manual with 25 Sample Experimental Program.
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**Term and condition**

**1. Inclusive of all Price Valid GSTIN/IT Clearance should be submitted.**

**2. Warranty of minimum one Year**.

1. **The concerned firm should submit the proof of deposit of CGST &SGST against bill amount within 30 days from the date release of payment.**

By Order of Principal

 Sd/-

 **HOD, CSA**